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Modeling and design of an re-configurable isolated remote for plasma experiments with hard-real-time synchronization

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The purpose of this paper is to present the design and implementation of a reconfigurable remote control for performing plasma experiments with Hard-Real-Time (HRT) synchronization under jitter less than 1 microsecond. An additional requirement for a multichannel synchronization system is the use of high-speed optical converters to provide galvanic isolation between powerful modules of the setup and remote control in order to exclude any possibility of disruption of the physical experiment control system.

Modeling and development of the software part of the maser remote control panel was performed in the LabVIEW application development environment with Real Time and FPGA modules.

The hardware part of the control panel is implemented on a real-time controller working in conjunction with the Xilinx FPGA module. To ensure the optical isolation of synchronization signals, boards of electron-optical converters based on LED lasers with fiber-optic terminals were developed and manufactured.

The control program is implemented in a two-module architecture with a HOST application and an FPGA application that exchange data over a 1000BASE-T Ethernet network.

Key words and phrases: remote control, synchronization, hard real-time system, FPGA, reconfigurable input-output (RIO)

1. Introduction

Control of a complex multi-parameter physics experiment places high demands on the synchronization of the operation of various systems, nodes and modules of the experimental setup [1]–[3]. In such case, controllability is defined as the ability of the remote control system to achieve a define state when several processes work together or in a certain experiment scenario in a time sequence [4]. Real-time systems must accomplish executive and application tasks within specified timing constraints [5], [6]. The time resolution —

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the minimum distinguishable step along the time axis-is in this case the most important characteristic of the synchronization system. So simultaneity of processes or phenomena is reduced to the fact that these processes on the time axis are separated from each other by intervals that do not exceed the time resolution of the system.

The use in physics experiment high-voltage AC and DC or UHF power systems generates a high level of electromagnetic noise and in this case the concept of Power-over-Fiber technology is applied to ensure controllability [7], [8]. Thus, the safety regulations for equipment operating at high power levels, along with the serious requirements for the reliability of the electronics in harsh environments, require galvanic isolation [9], [10].

Our work on synchronization in HRT systems has been performed as part of an ongoing autoresonance plasma experiments in Plasma physics laboratory of RUDN [11], [12]. To organize the operation of the synchronization system, a single time reference point is selected, and the characteristic stages of each of the processes are separated from this beginning by an adjustable amount of delay. The device that provides a countdown of the required delay was called a delay generator. The developed concept can easily be scaled practically on any type of complicated plasma experiments.

2. Experimental setup

As an example of a complex physical installation that requires control with real time synchronization, consider the problem of controlling a plasma maser. A plasma maser (plasma relativistic microwave generator) is a source of powerful microwave radiation in which the Cherenkov effect of the interaction of a high-current relativistic electron beam (REB) with a slow wave of a plasma waveguide is realized [13]–[15]. The configurations of plasma masers differ in the relative position of the REB and plasma and are described in detail in [16], [17].

In the maser scheme implemented in this project, the REB propagates in a strong longitudinal magnetic field in a metal cylindrical waveguide, in which a plasma with controlled parameters is pre-created.

A simplified block diagram of the created plasma maser is shown in the figure 1 and includes an main experimental setup and a system for ensuring its operation.

The main experimental setup includes 4 modules:

- A generator of periodic high-voltage pulses of nanosecond duration as a source of a high-current REB in a direct-acting accelerator based on an explosive-emission cathode.
- A plasma source with controlled parameters.
- The space of formation of REB and plasma in a strong magnetic field.
- Remote control of all nodes and diagnostic systems.

The support system consists of:

- A vacuum pumping system.
- Gas inlet system for creating plasma.
- A system for converting a plasma wave into an electromagnetic wave, releasing radiation into the atmosphere, focusing it, and transporting it.
- Radiation detection and diagnostics system.



Figure 1. Block diagram of a plasma maser

The operation of all the main operating systems of the plasma maser is provided by a synchronization system with a strict reference to real time. This system is integrated into the control panel, where the operator sets the required operation scenario, provides a physical start of the maser and receives a report in the form of tabular and graphical data on the state of the maser subsystems at synchronous times of the working cycle. Schematically, the maser operation scenario is shown in the figure 2.



Figure 2. Plasma maser working scenario

If the warm solenoid is ready, after 30 seconds after all the limit switches (locks) are triggered, a ready signal is generated — the "Start" button in the

remote control interface is activated. Pressing the "Start" button generates a "glow" pulse, which is applied to the power supply of the plasma source.

After the set time "Glow duration" (set by the operator in the range of 0-5 s), the signal "solenoid current" is generated. After the battery charge current of the solenoid reaches the set values, the remote control generates a burst of pulses with the set frequency during 1 s.

The developed remote control allows the operator to implement two options for starting the maser. In case 1 (see the figure 2) a beam of relativistic electrons interacts with a "quiet" plasma in the absence of current in the cathode circuit of the plasma generator. The plasma decay time is of the order of tens to hundreds of microseconds, so synchronization with a jitter of the order of 1 microsecond is enough. In the development of the launch scenario according to the case 2 option, the REB interacts with the plasma under conditions of increasing concentration. One of the tasks of the experiment is to compare the two described scenarios and identify the preferred conditions for the occurrence and development of a plasma-beam discharge in terms of obtaining a powerful broadband EMR pulse.

After a time of about 1 ms after the last pulse of the bundle, the remote control generates the endings (trailing edges) of the current pulses of both warm solenoids and the glow pulse of the thermocathode.

The developed remote provides control of the signals of the security system, as well as the generation of appropriate enabling, warning, or prohibiting signals and commands.

The NI cRIO-9053 chassis (see the figure 3(a)) with a real-time controller and an integrated field-programmable gate array (FPGA) chip was chosen as the hardware platform of the synchronization system. The FPGA architecture is a set of programatically configurable logic blocks, the connections between them, and the I/O blocks. This structure is best suited to the tasks of parallel multi-channel data processing to multi-channel signal generation.



Figure 3. NI cRIO-9053 chassis (a); I/O modules NI 9401 and NI 9402 (b)

The reconfigurable I/O modules NI 9401 and NI 9402 were used for matching with the generator loads — input channels of synchronized devices (see the figure 3(b)). They have a similar circuit architecture but differ in the maximum switching speed and the form factor of the output connectors. This solution is chosen to conditionally divide the input and output signals into two groups: "fast" signals with strict jitter requirements (starting the plasma generation and starting the REB source) and "slow" signals with a relatively less rigid reference to real time (starting the heating of the thermal cathode, starting the solenoid current generator and emergency lock signals).

Main characteristics of the controller and modules:

- 4-slot CompactRIO controller, 1.30 GHz Dual-core CPU, 4 GB DRAM, 4 GB Storage, -20°C to 55°C, Artix-7 50T FPGA. The cRIO-9053 controller is a secure, high-performance, customizable embedded controller that contains a dual-core Intel Atom processor, an Artix-7 FPGA, and four slots for C-series modules. It runs on the NI Linux Real-Time operating system with I/O access via the NI-DAQmx drivers or via the LabVIEW FPGA module.
- The NI 9401 module provides a reconfigurable I/O interface for digital lines in 4-bit increments and operates in three configurations: 8 digital inputs, 8 digital outputs, or 4 digital inputs and outputs each. The pulse font is not worse than 100 ns. The signal level is 5 V TTL. Output connector 25 PIN D-SUB.
- The NI 9402 module provides a reconfigurable I/O interface for 4 digital lines, operating in two configurations: 4 digital inputs, or 4 digital outputs. The pulse font is not worse than 50 ns. The signal strength is 3.3 V LVTTL. 4X BNC (50 Ohm) output connector.

A digital 4-channel RIGOL MSO1074 oscilloscope with a bandwidth of 70 MHz and a minimum detectable pulse duration of 10 ns was used to monitor the system and perform test measurements.

3. Results and conclusion

A program of control of the prototype generator was created in the Lab-VIEW graphical programming environment [18]–[20]. LabVIEW is one of the most popular development environments for modeling, simulation and equipment control applications.

The developed program consists of two parts: the top-level host program in the figure 4 is responsible for the user interface and transmits the synchronization system settings and setpoints to the FPGA program once per second. The current values of the settings are transmitted over the communication channel between the HOST and the FPGA applications.

In the FPGA program in the figure 5 after checking the health of all connected devices and assigning program identifiers to the physical synchronization channels, an infinite loop begins, working out the commands of the HOST application. After receiving the "START" command, the synchronization signals are switched on and off sequentially with the specified durations and delays for each of the active channels. After the plasma maser cycle is completed, the FPGA application signals this to the HOST application and goes back to standby mode.

The waveforms with the results of testing the program of the remote control are shown in the figure 6. Beam 1 — the starting pulse, beam 2 — the output pulse of one of the synchronization channels (starting the plasma generation). Tests have shown that the programmable delay of the generated signal corresponds to the setpoint with high accuracy.









The jitter observed at the signal edges is less than 100 ns (it is not noticeable on the waveforms). The step of changing the programmable delay in the synchronization system channel is 1 microsecond. The delay can vary from 1 microsecond to 2^{32} microseconds (more than 1 hour).



Figure 6. Waveforms of the operation of the plasma generation start channel in single-channel mode with a delay time setting of 450 microseconds

The next stage of our tests was to check the software control of the delays in the generation of the REB relative to the front of the plasma pulse under the conditions of the generation of a pulse train. To configure program delays, the control program interface provides a function block, shown in the figure 7.



Figure 7. The control program interface block, with channel delay settings

After specifying the number of pulses in the packet, the values of the plasma pulse durations and lead times (by how many microseconds the front of the plasma pulse is ahead of the RAP pulse) should be entered in the program delay table for each pulse of the packet. The settings made in the first row can be applied to the entire table. It is possible to read the corresponding table from a text file in a similar format to the table. The separator is a Tab character, the line separators are the End Of Line character.

A series of waveforms showing the possibilities of software tuning of the advance is shown in figures 8, 9, 10. The beam 2 is a synchropulse for plasma generation, the beam 1 is a program repeater of the REB pulse with a duration of 10 microseconds (for easy display on oscillograms).

It can be seen that the leading edge of the plasma pulse of the REB pulse strictly follows the setpoints of the control program interface (see the figure 7).

The created software and hardware complex allows you to scale the number of synchronization channels and, with the available equipment, get a total of 8 delay channels with edges no worse than 50 ns and 16 channels with edges no worse than 100 ns. The control panel based on the implemented synchronization system has shown the ability for long-term stable uninterrupted operation and is currently successfully used in experiments to select the optimal operating modes of a plasma maser. Additionally, if it is necessary to switch to a submicrosecond delay control step, the system can be reconfigured to work with synchronization directly from the FPGA clock generator. The existing FPGA platform with a clock frequency of 40 MHz allows you to reduce the minimum step of regulating pulse durations and delays to 25 ns. It should, however, be taken into account that the control range in this case will decrease by a multiple to a value of about 15 minutes.



(a) the first five pulses of the pulse train with a controlled advance of the REB pulse (beam 1) by the pulse front of the plasma generator (beam 2)





Figure 8. The possibilities of tuning the advance between channels in the pulse train generation mode





(b) the 3-rd pulse of the pulse train with advances of 250 microseconds





(a) the 4-th pulse of the pulse train with advances of 350 microseconds



(b) the 5-th pulse of the pulse train with advances of 450 microseconds



The developed multi-channel synchronization system with fiber-optic galvanic isolation of the controller and secondary circuits of the equipment can be easily adapted to any tasks of controlling a physical experiment. This, in particular, is in demand when conducting experiments with cold plasma in the IFIT RUDN. These experiments require synchronization of the operation of a high-power microwave generator and a pulse current generator, as well as synchronized measurements of radiation parameters from the region of the plasma clot localization in the microwave, optical and X-ray ranges, probe measurements in the low-frequency and microwave ranges, etc. With the help of the created system, it is possible to provide such synchronization with a time resolution of at least 1 microsecond.

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Моделирование и разработка реконфигурируемого пульта управления для плазменных экспериментов с жёсткой синхронизацией в реальном времени

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Цель данной статьи — представить дизайн и реализацию реконфигурируемого пульта дистанционного управления для проведения плазменных экспериментов с синхронизацией в режиме жёсткого реального времени при джиттере менее 1 микросекунды. Дополнительным требованием к системе многоканальной синхронизации является использование высокоскоростных оптических преобразователей для обеспечения гальванической развязки между мощными модулями установки и дистанционного управления, чтобы исключить любую возможность нарушения работы системы управления физическим экспериментом.

Моделирование и разработка программной части пульта дистанционного управления мазером проводились в среде разработки приложений LabVIEW с модулями Real Time и FPGA.

Аппаратная часть панели управления реализована на контроллере реального времени, работающем совместно с модулем Xilinx FPGA. Для обеспечения оптической развязки сигналов синхронизации разработаны и изготовлены платы электронно-оптических преобразователей на основе светодиодных лазеров с оптоволоконными выводами.

Программа управления реализована в двухмодульной архитектуре с приложением HOST и приложением FPGA, которые обмениваются данными по сети 1000BASE-T Ethernet.

Ключевые слова: пульт управления, синхронизация, система жёсткого реального времени, настраиваемый ввод-вывод